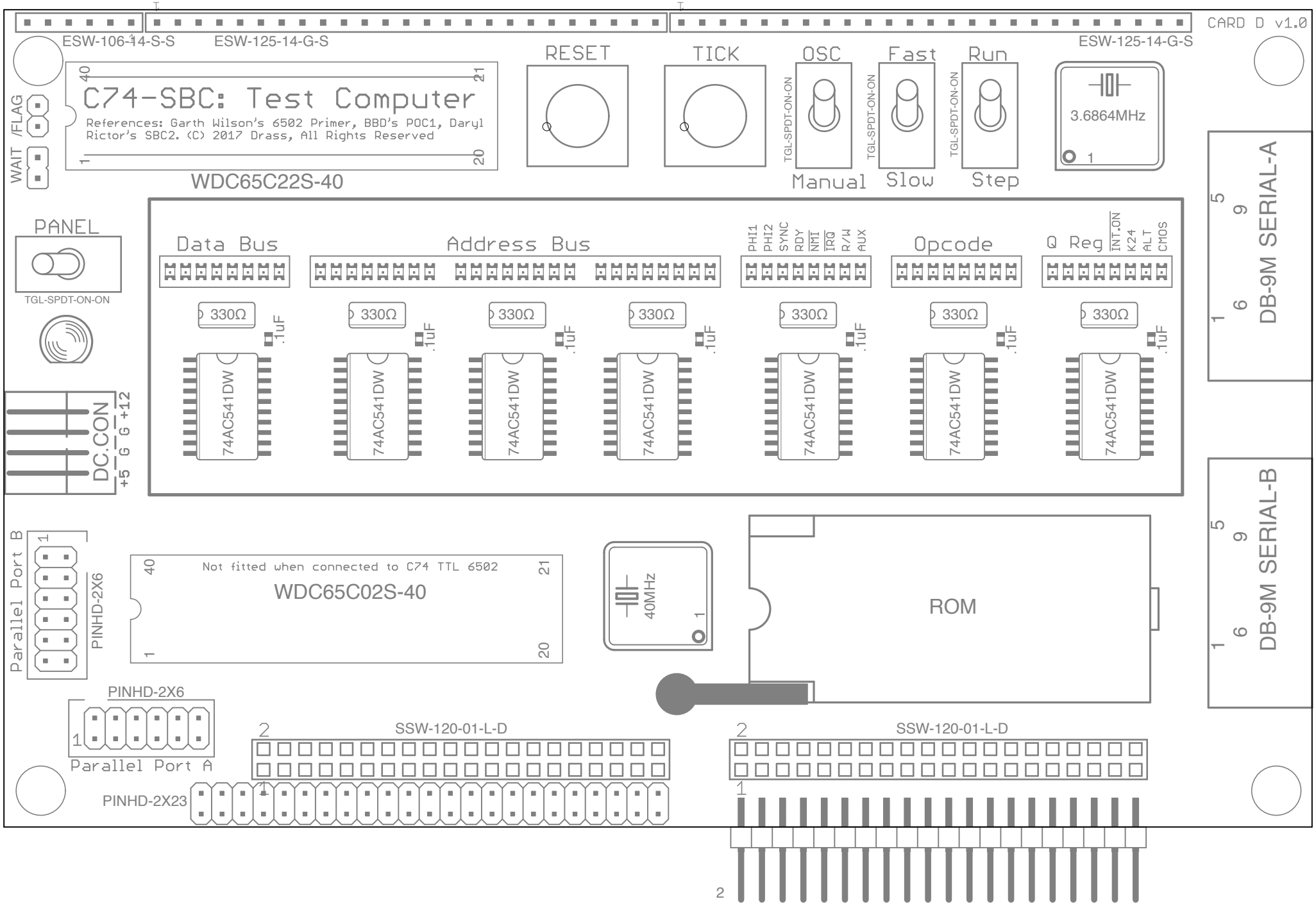
CARD D v1.0
ESW-106-14-S-S
ESW-125-14-G-S
ESW-125-14-G-S
C74-SBC: Test Computer
References: Garth Wilson's 6502 Primer, BBD's POC1, Daryl Rictor's SBC2. (C) 2017 Drass, All Rights Reserved
WDC65C22S-40
WAIT /FLAG
RESET
TICK
OSC
Fast
Run
TGL-SPDT-ON-ON
Manual
Slow
Step
3.6864MHz
PANEL
TGL-SPDT-ON-ON
DC.CON
+5 G G +12
Data Bus
Address Bus
PHI1 PHI2 SYNC RDY NMI IRQ R/W AUX
Opcode
Q Reg
INT.ON K24 ALT CMOS
330Ω
.1uF
74AC541DW
DB-9M SERIAL-A
DB-9M SERIAL-B
Parallel Port B
PINHD-2X6
Not fitted when connected to C74 TTL 6502
WDC65C02S-40
40MHz
ROM
PINHD-2X6
Parallel Port A
SSW-120-01-L-D
SSW-120-01-L-D
PINHD-2X23